

| | L # | Hit | earch Text | DBs |
|----------|------------|-------------|--|---|
| 1 | L1 | 3320 | (257/691,723,724,777,924).ccls. | USP AT; US-P GPU B |
| 2 | L2 | 1990 | (361/760,763,782,783).ccls. | USP AT; US-P GPU B |
| 3 | L3 | 808 | 327/536.ccls. | USP AT; US-P GPU B |
| 4 | L4 | 771 | (363/59,60).ccls. | USP AT; US-P GPU B |
| 5 | L5 | 6531 | 1 2 3 4 | USP AT; US-P GPU B |
| 6 | L6 | 266 | 5 and @pd>=20031003 | USP AT; US-P GPU B |
| 7 | L7 | 34 | 6 and ((charge adj pump\$1) (voltage adj (booster\$1 multipli\$6 doubl\$3)) (pump with circuit\$1)) | USP AT; US-P GPU B |

| | L # | Hit | Search Text | DBs |
|-----------|------------|------------|--|---|
| 8 | L8 | 232 | 6 not 7 | USP AT; US-P GPU B |
| 9 | L9 | 91 | 8 and (passive inductor\$1 capacitor\$1 condenser\$1) | USP AT; US-P GPU B |
| 10 | L10 | 141 | 8 not 9 | USP AT; US-P GPU B |
| 11 | L11 | 2 | 10 and ((inductive capacitive) adj (element\$1 component\$1)) | USP AT; US-P GPU B |
| 12 | L12 | 139 | 10 not 11 | USP AT; US-P GPU B |
| 13 | L13 | 7 | 12 and (inductive\$2 capacitive\$2) | USP AT; US-P GPU B |
| 14 | L14 | 132 | 12 not 13 | USP AT; US-P GPU B |

| | L # | Hit | S arch Text | DB |
|----------|------------|-------------|--|---|
| 1 | L1 | 1826 | (438/106,109).ccls. | USP AT; US-P GPU B |
| 2 | L4 | 5 | 1 and ((charge adj pump\$1) (voltage adj (booster\$1 multipli\$6 doubl\$3)) (pump with circuit\$1)) | USP AT; US-P GPU B |
| 3 | L5 | 1821 | 1 not 4 | USP AT; US-P GPU B |
| 4 | L6 | 343 | 5 and (passive inductor\$1 capacitor\$1 condenser\$1) | USP AT; US-P GPU B |